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Date: April 6, 2005

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Re:	USSN: 10/828,773		

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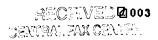
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	Application Number	10/828,773				
TRANSMITTAL	Filing Date	April 20, 2004				
FORM	First Named Inventor	Badredin Fatemizadeh				
(to be used for all correspondence after initial filing)	Art Unit	2826				
	Examiner Name	Fetsum Abraham				
Total Number of Pages In This Submission 2	Attorney Docket Number	112518.00005				
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Fee Transmittal Form	Drawing(s)	After Allowance communication to Technology Center (TC)				
Fee Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences				
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or Robert D. Atkins, Reg. 34,2	88 - QUARLES & BRADY S	STREICH LANG LLP				
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Pate April (. 2005						
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Maritza O'Neill						
Signature	D'huil!	Date April (,, 2005)				
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Fatemizadeh et al.

Serial No.:

10/828,773

Filed:

April 20, 2004

For:

VERTICAL POWER JFET WITH LOW ON-RESISTANCE

FOR HIGH VOLTAGE

APPLICATIONS

Customer No.: 26707

Confirmation No.: 5237

Examiner: Fetsum Abraham

Art Unit: 2826

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed March 22, 2005, Applicant hereby elects, without traverse, the claims classified as Group II in the Office Action. Namely, Applicant elects Claims 6-32 drawn to a transistor, classified in class 257, subclass 137.

Applicant hereby withdraws Claims 1-5 from further consideration in this Office Action, but reserves the right to prosecute these claims in one or more divisional applications, which may be filed before the close of prosecution of the instant application.

Respectfully submitted,

QUARLES_& BRADY STREICH LANG LLP

April 6, 2005

Robert D. Atkins

Reg. No. 34,288

Attorney Docket No. 112518.00005

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